

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Thomas P. Glenn, Steven Webster

Assignee:

Amkor Technology, Inc.

Title:

Tool And Method For Forming An Integrated Optical Circuit

Serial No.:

09/751,537

Filing Date:

December 29, 2000

Examiner:

Not Yet Assigned

Group Art Unit: Not Yet Assigned

Docket No.:

M-8933 US

San Jose, California March 30, 2001

Box Missing Parts

Attn: Official Draftsperson

COMMISSIONER FOR PATENTS

Washington, D. C. 20231

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit 6 sheets of formal drawings, consisting of Figures 1, 2, 3, 4a, 4b, 5a, 5b, 6a, 6b, 6c, 6d, 7, 8, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

EXPRESS MAIL LABEL NO:

Respectfully submitted,

EL 710 213 123 US

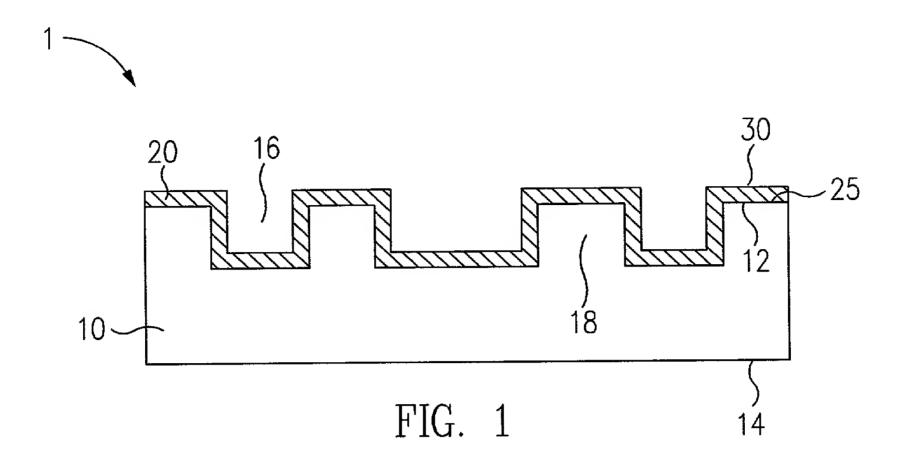
ames E. Parsons

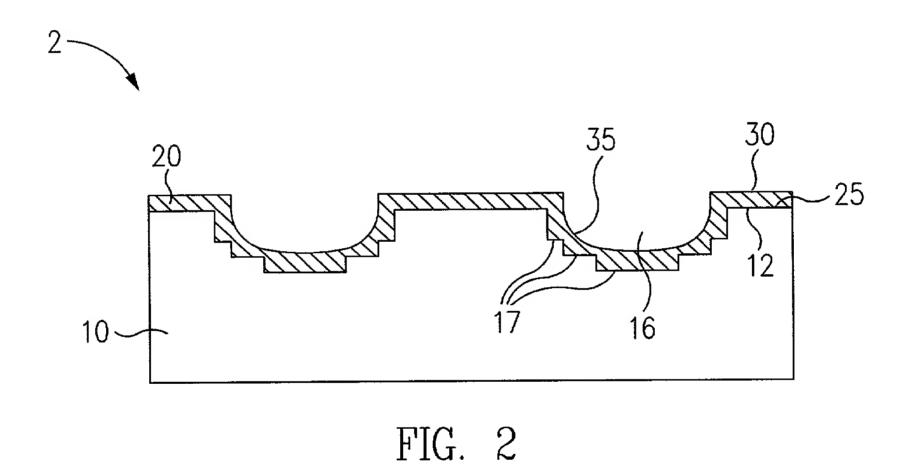
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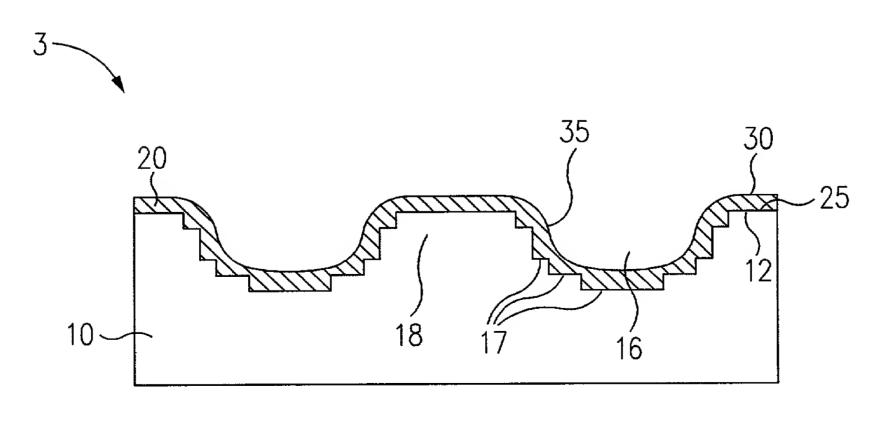


FIG. 3

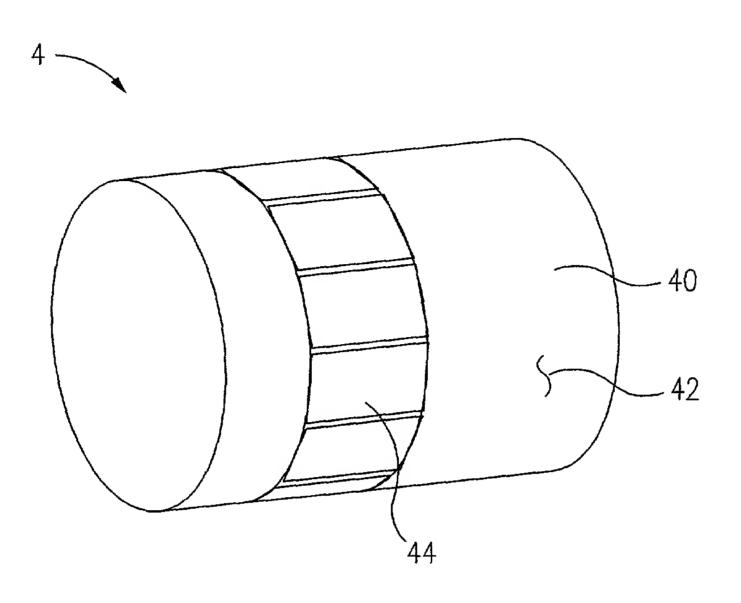


FIG. 4a

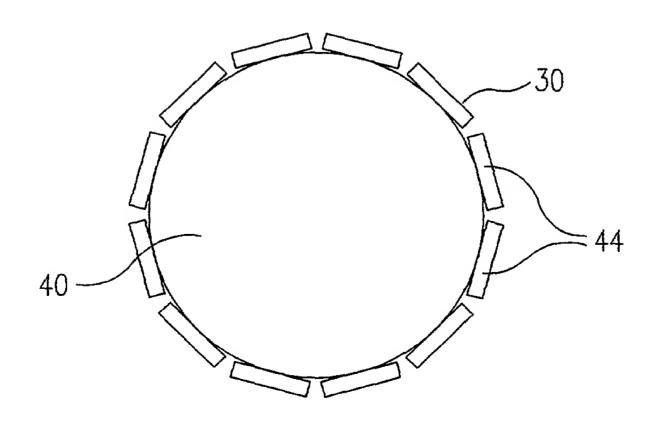


FIG. 4b

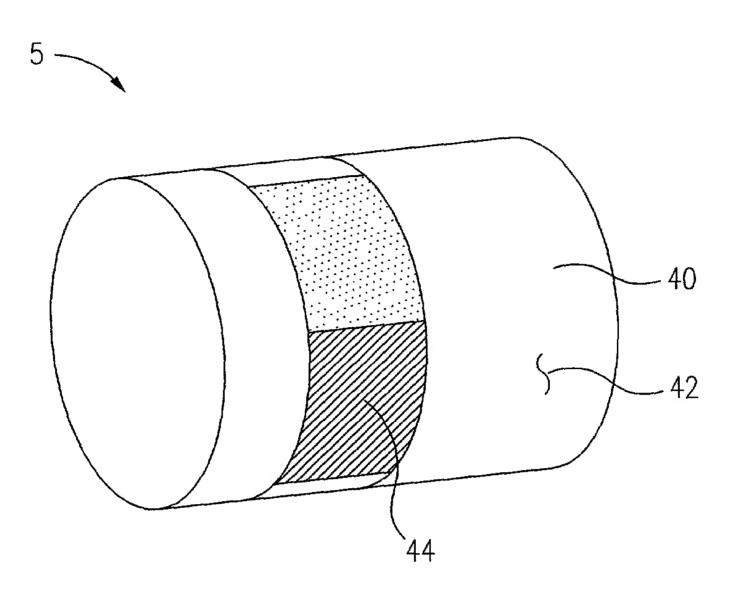


FIG. 5a

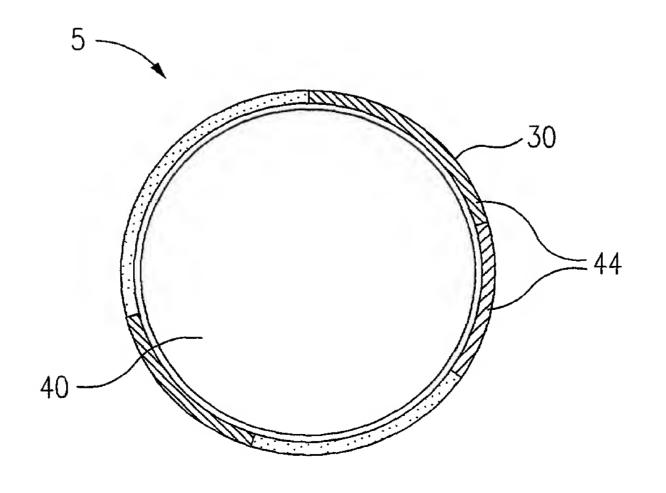


FIG. 5b

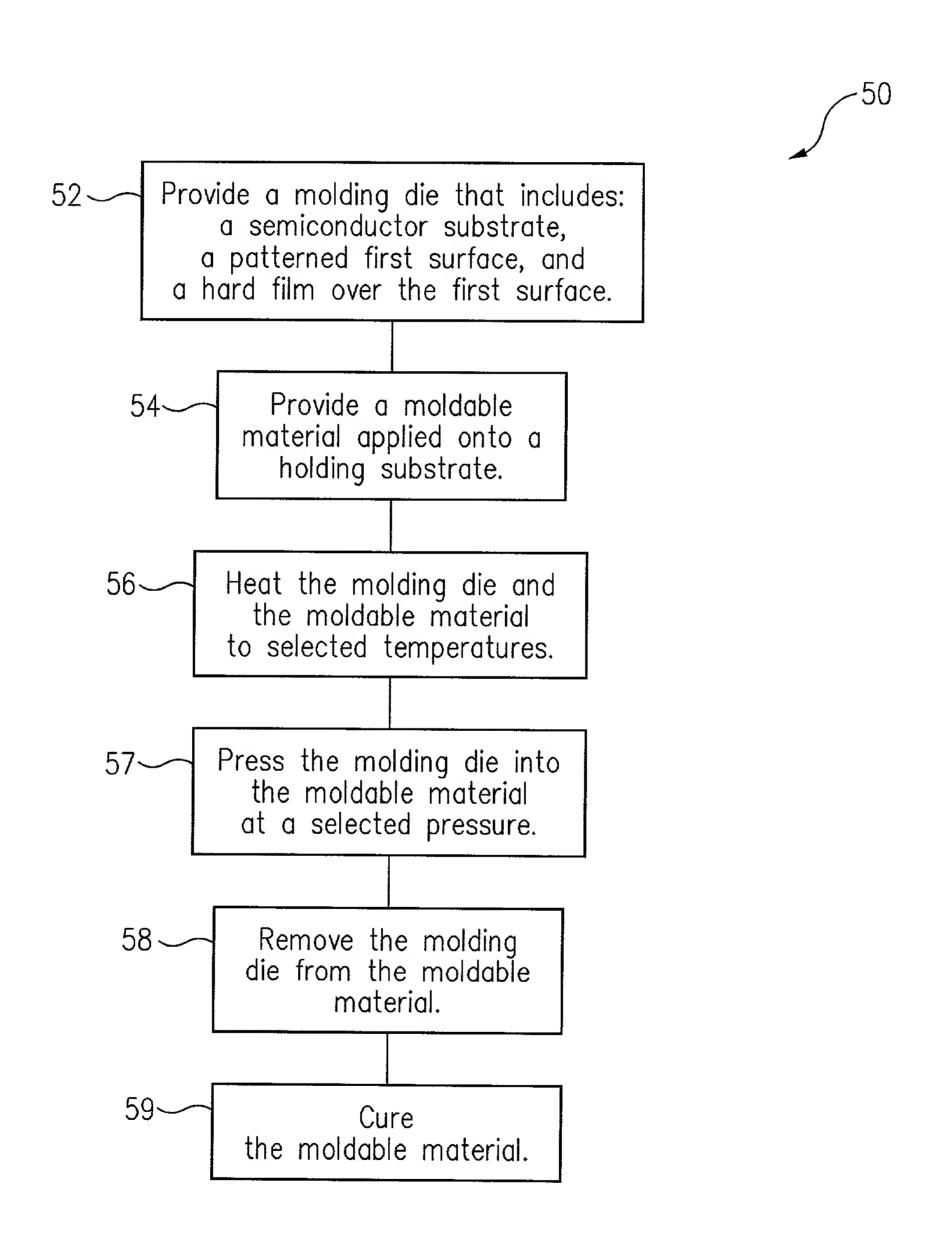


FIG. 6a

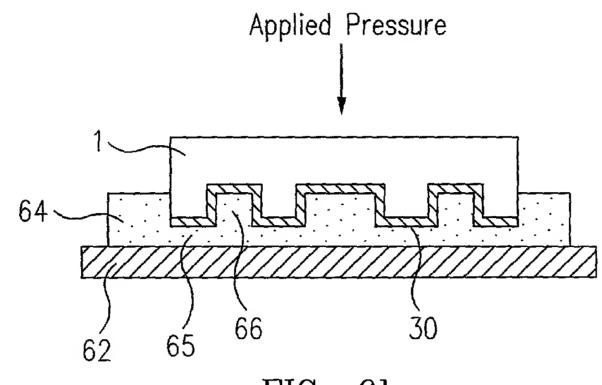


FIG. 6b

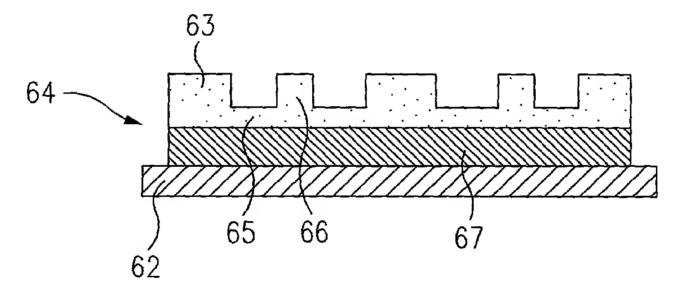


FIG. 6c

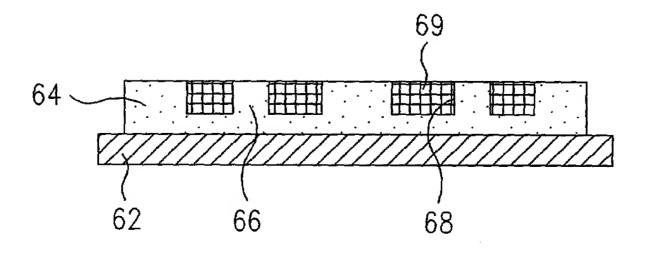


FIG. 6d

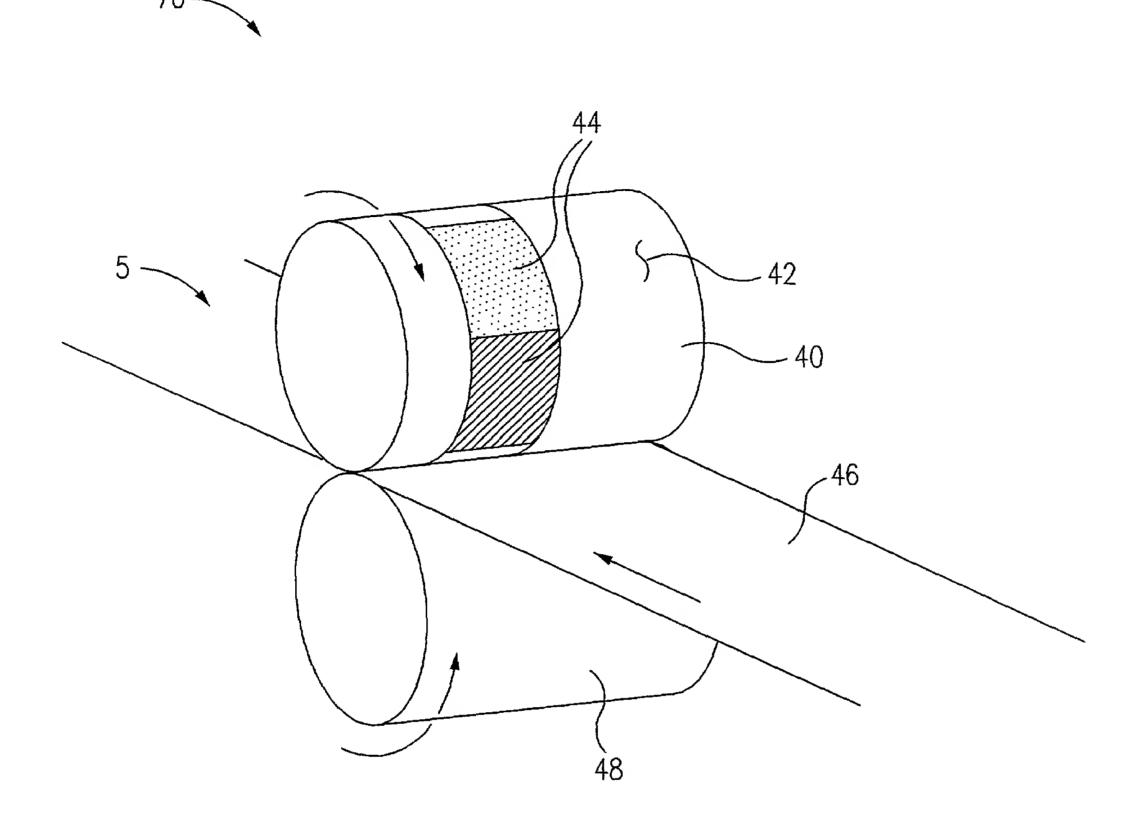


FIG. 7

